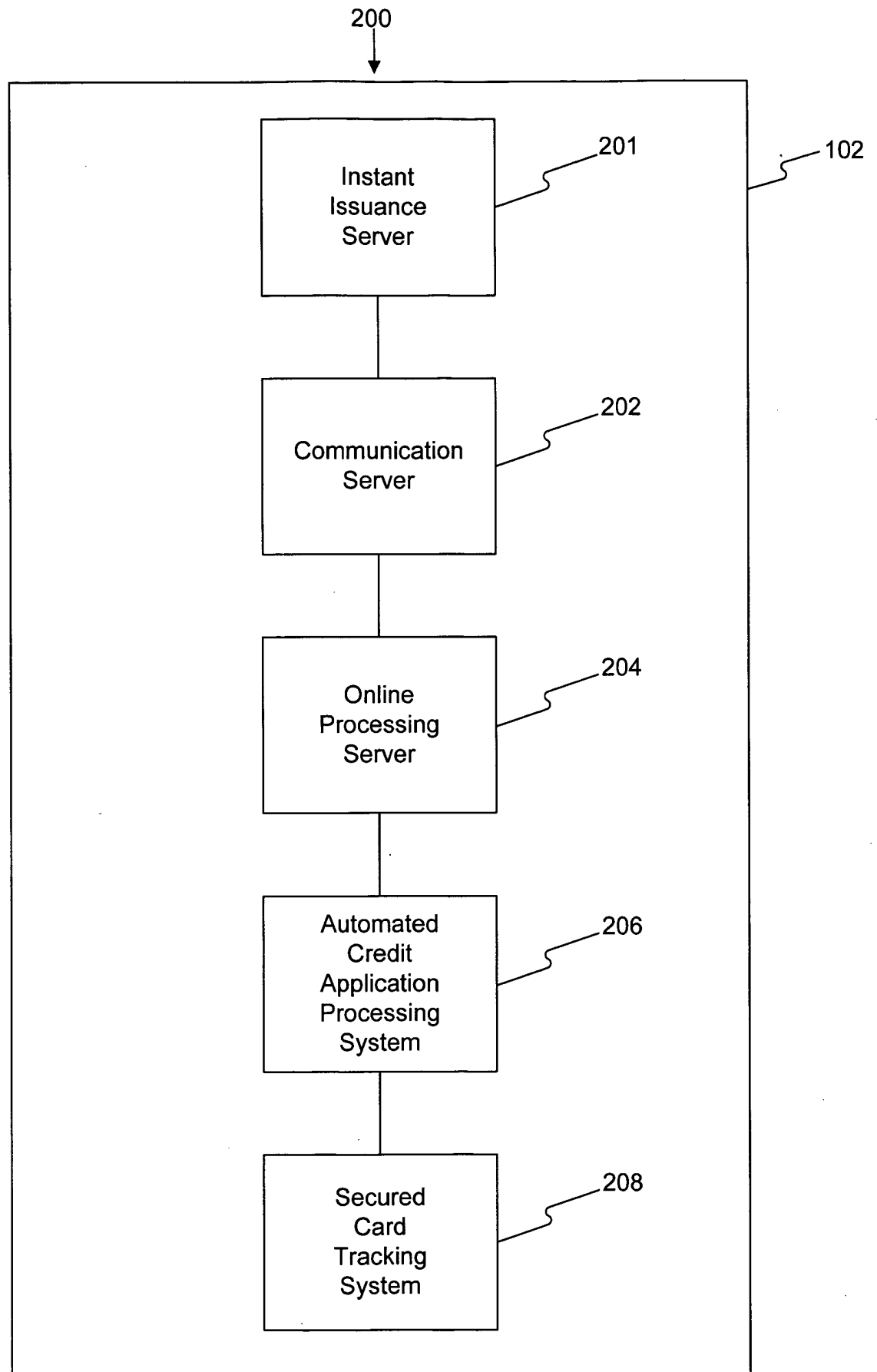
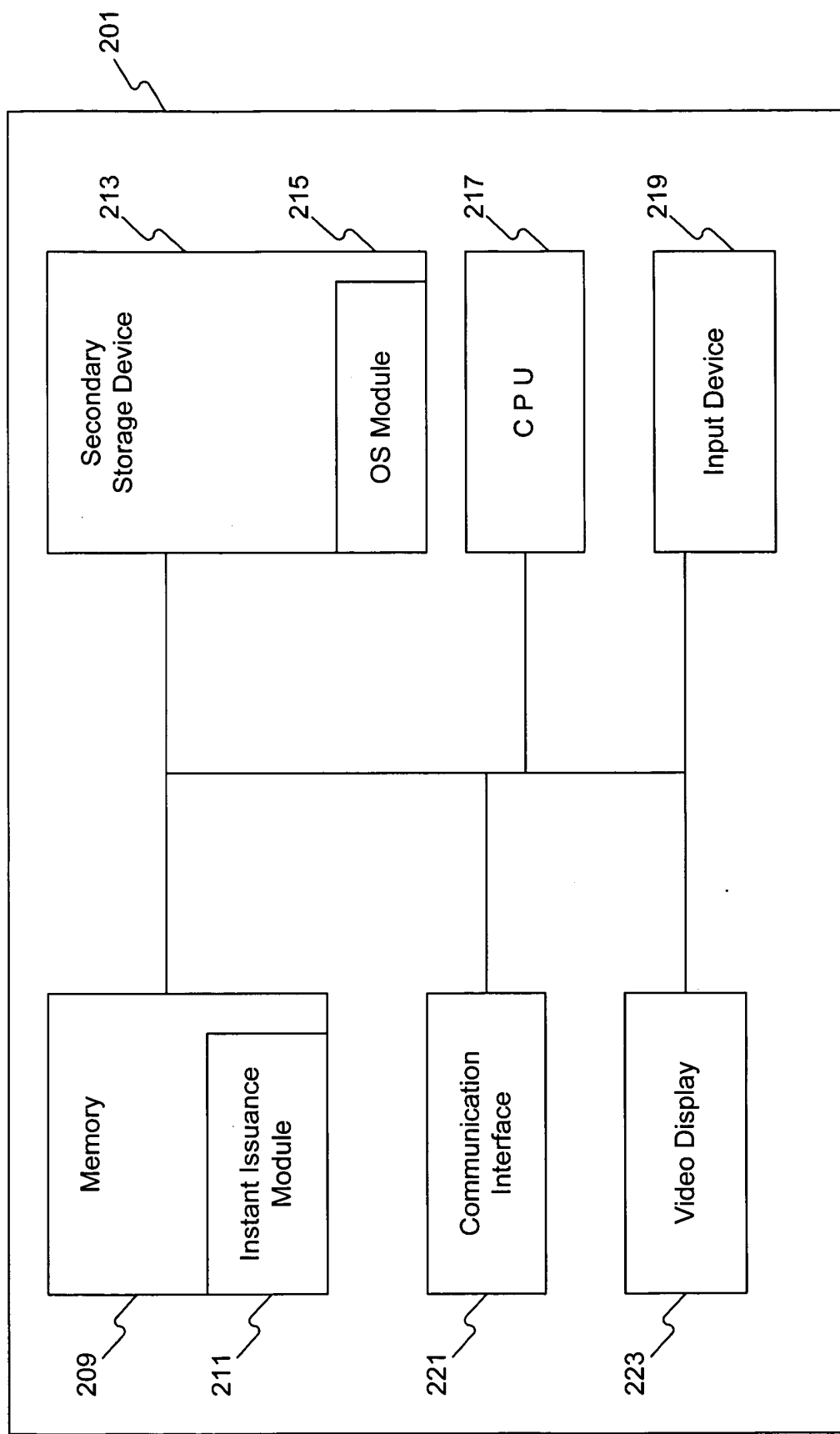


FIG. 1



**FIG. 2**



**FIG. 3**

FIG. 4 is a block diagram of a computer system 202. The system 202 includes a memory 210, a communication module 212, a secondary storage device 214, an OS module 216, a CPU 218, an input device 220, a communication interface 222, and a video display 224. The memory 210 and communication module 212 are connected to a system bus. The secondary storage device 214 and OS module 216 are also connected to the system bus. The CPU 218 is connected to the system bus. The input device 220, communication interface 222, and video display 224 are connected to the system bus.

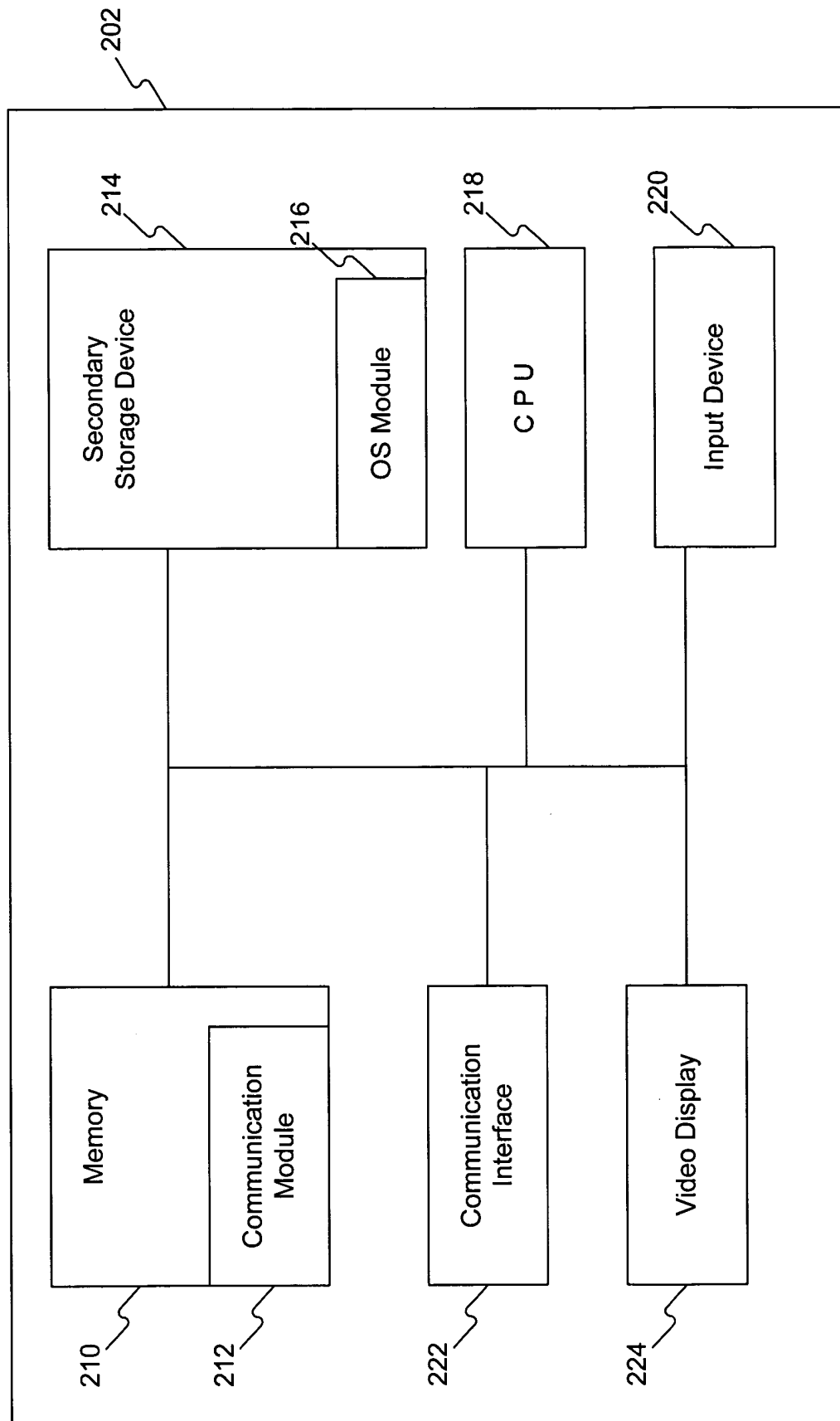


FIG. 4

FIG. 5 is a block diagram of a computer system 204. The system 204 includes a Memory 230, an Online Processing Module 232, a Secondary Storage Device 234, an OS Module 236, a CPU 238, an Input Device 240, a Communication Interface 242, and a Video Display 244. The Memory 230 and Online Processing Module 232 are connected to the Secondary Storage Device 234 and the OS Module 236. The CPU 238 is connected to the Secondary Storage Device 234 and the OS Module 236. The Input Device 240 is connected to the CPU 238. The Communication Interface 242 is connected to the CPU 238. The Video Display 244 is connected to the CPU 238.

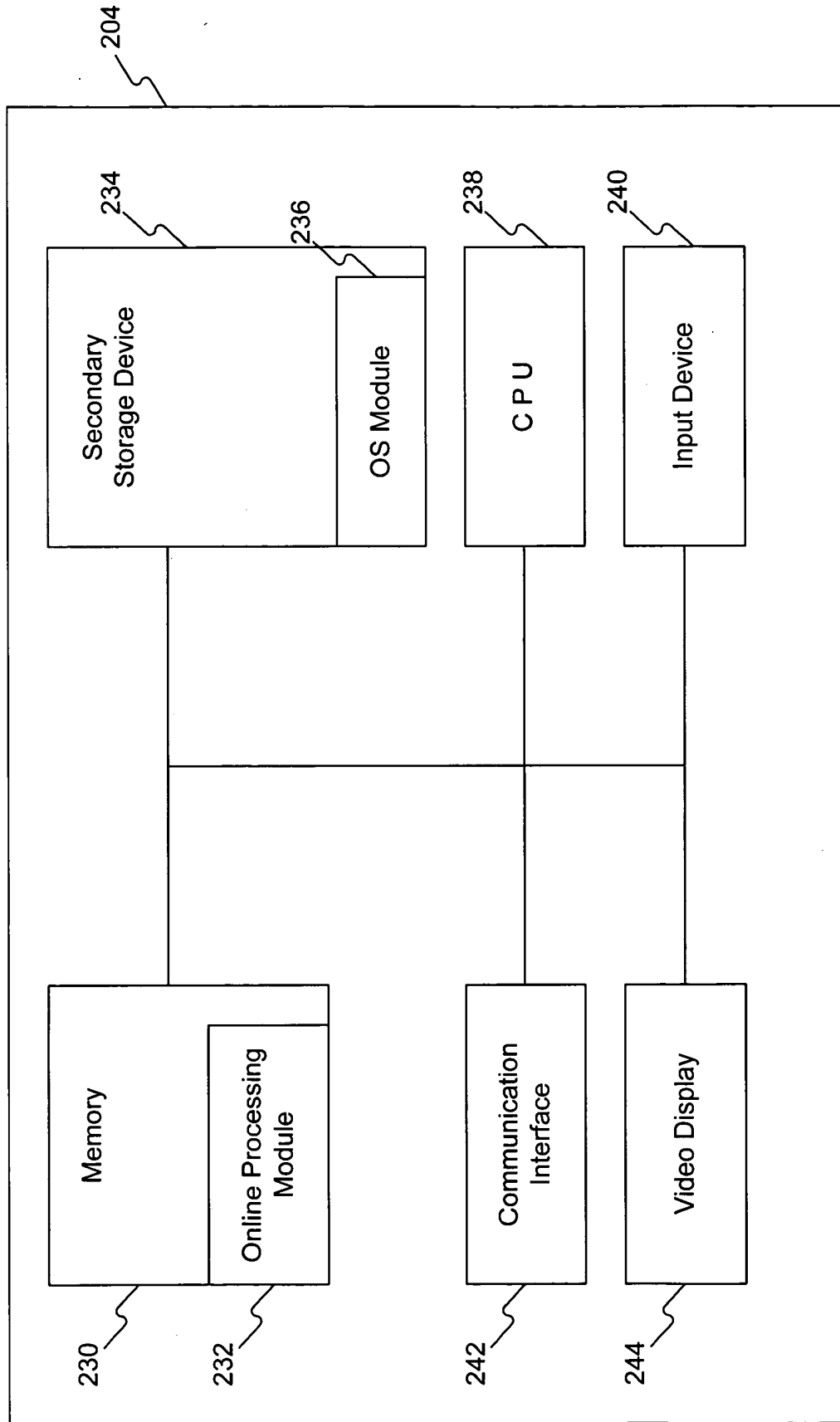


FIG. 5

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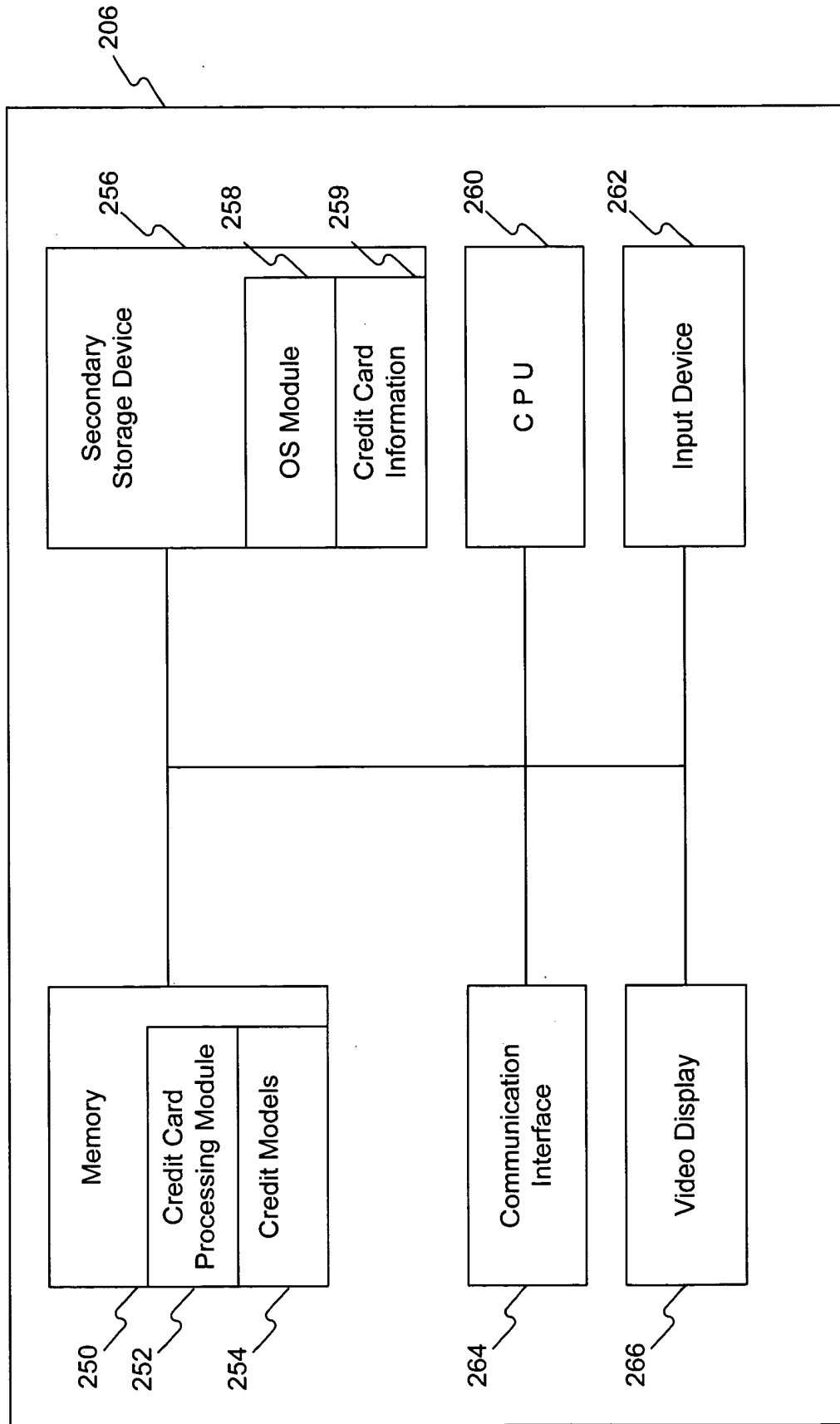


FIG. 6

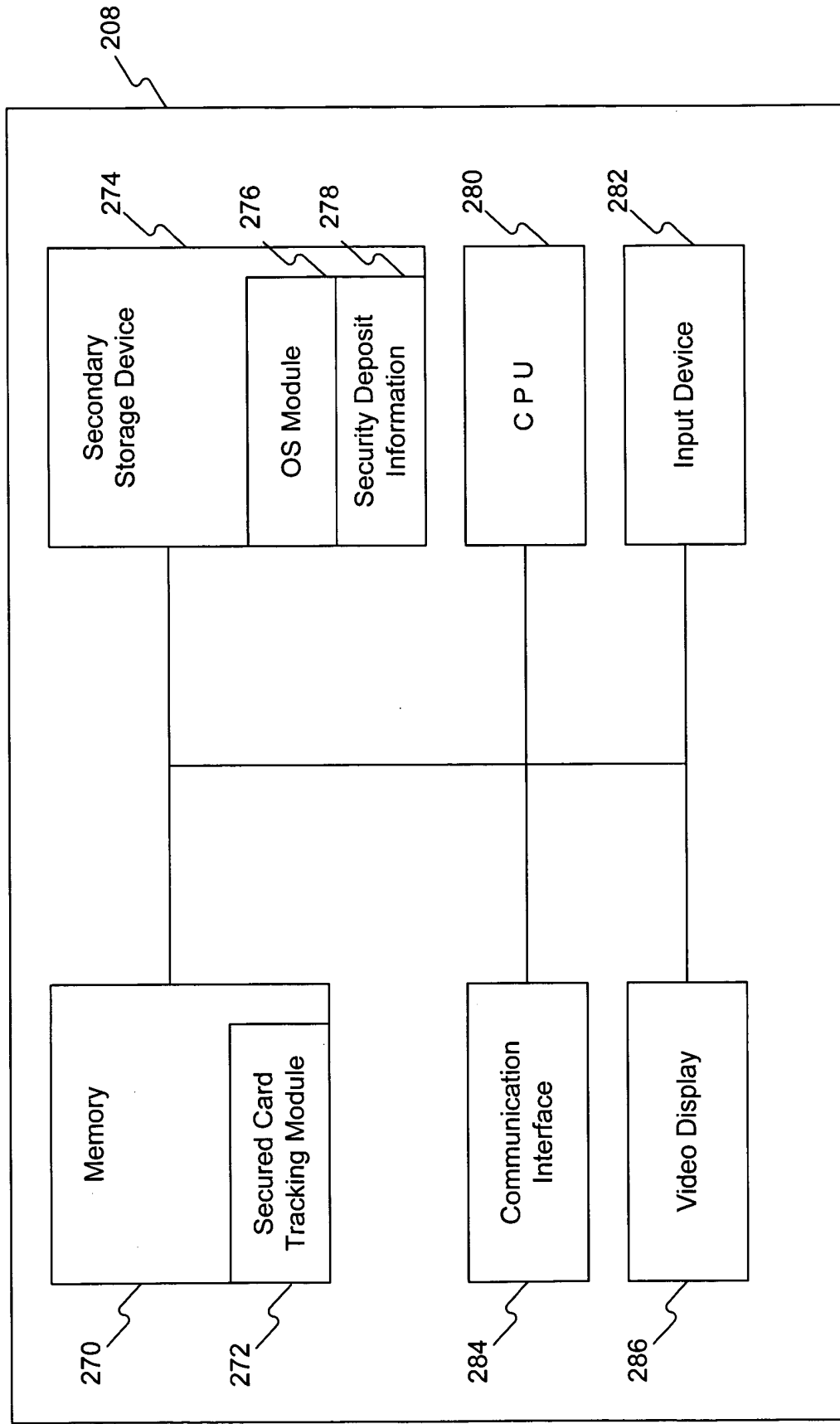


FIG. 7

FIG. 8 is a block diagram of a system 110, in accordance with an embodiment of the present invention. The system 110 includes a memory 132, a secondary storage device 134, a CPU 136, an embossing machine 138, a video display 142, a communication interface 140, and an input device 144. The memory 132 and secondary storage device 134 are connected to the CPU 136. The CPU 136 is connected to the embossing machine 138, the video display 142, and the communication interface 140. The communication interface 140 is connected to the input device 144.

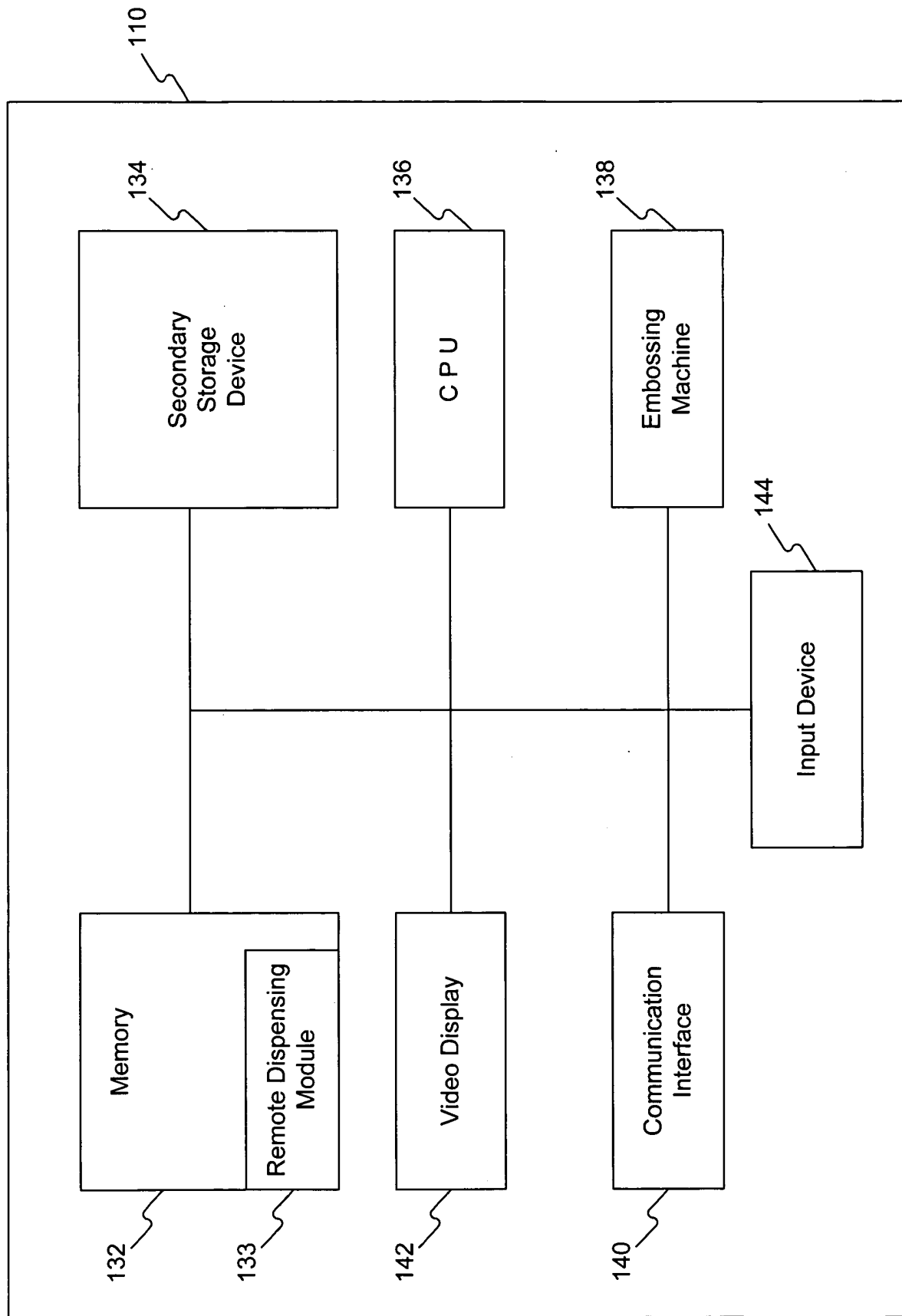


FIG. 8



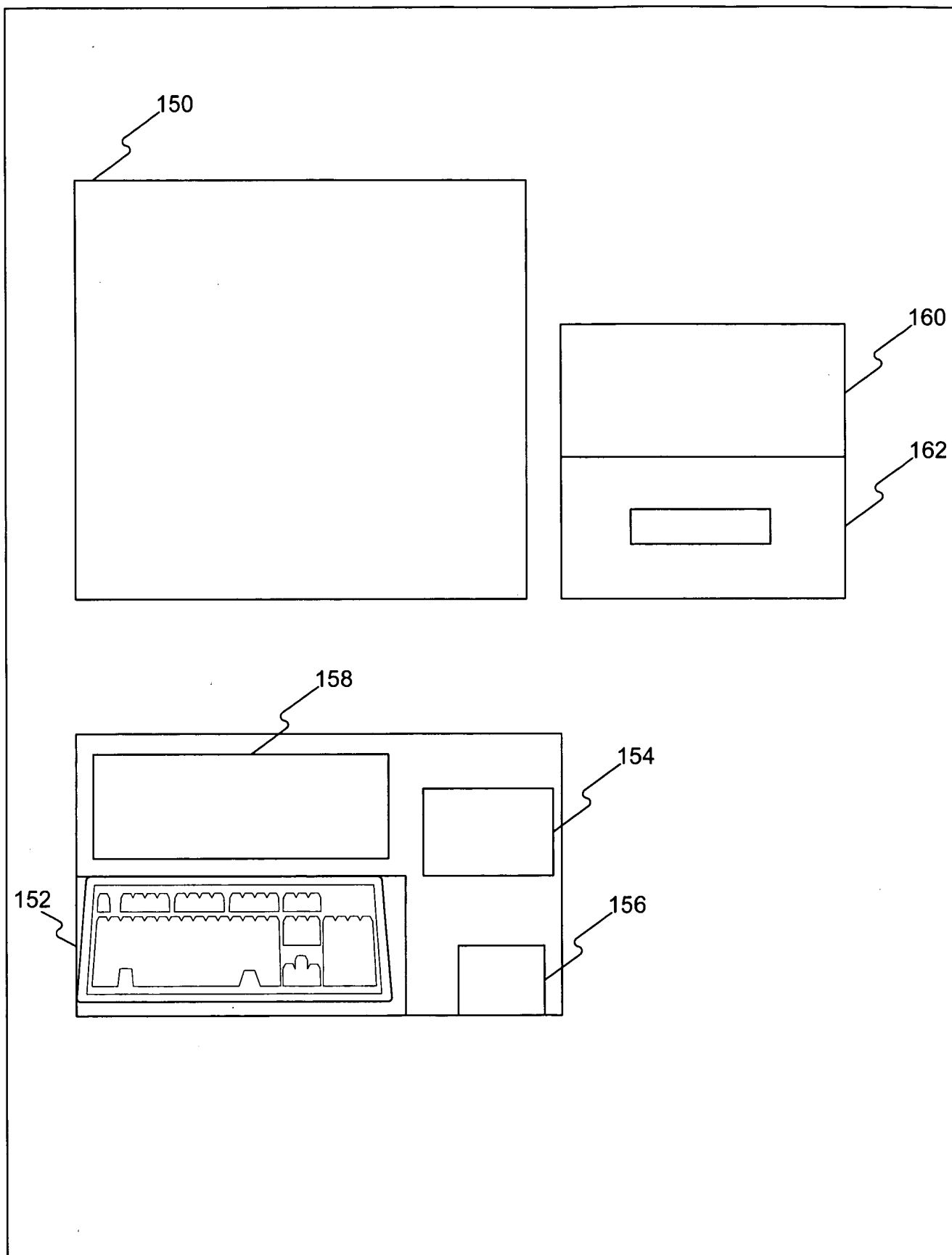
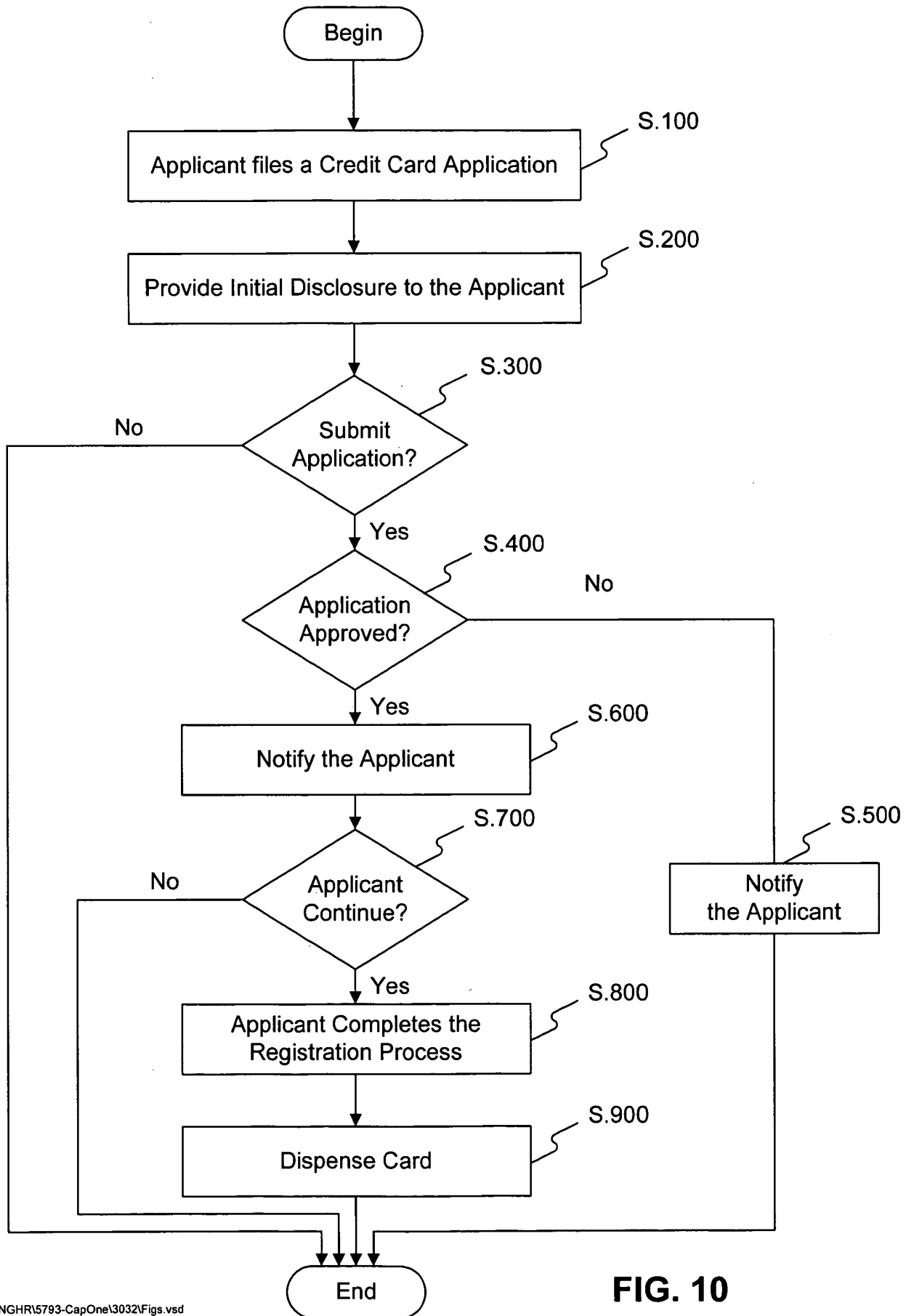


FIG. 9



**FIG. 10**